

Figure 1

Full-Duplex Current Loop  
(Prior Art)

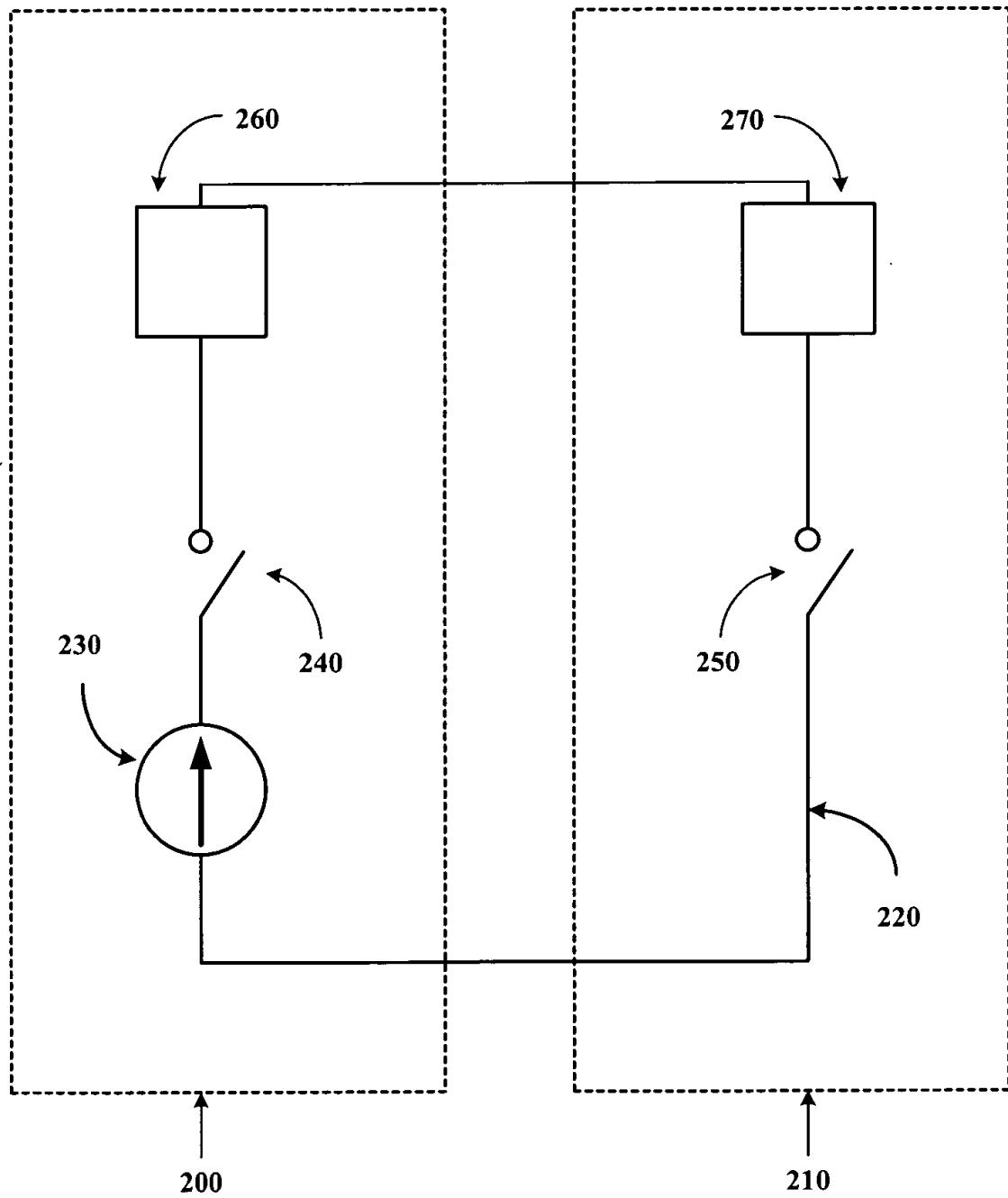


Figure 2

Simplex Current Loop  
(Prior Art)

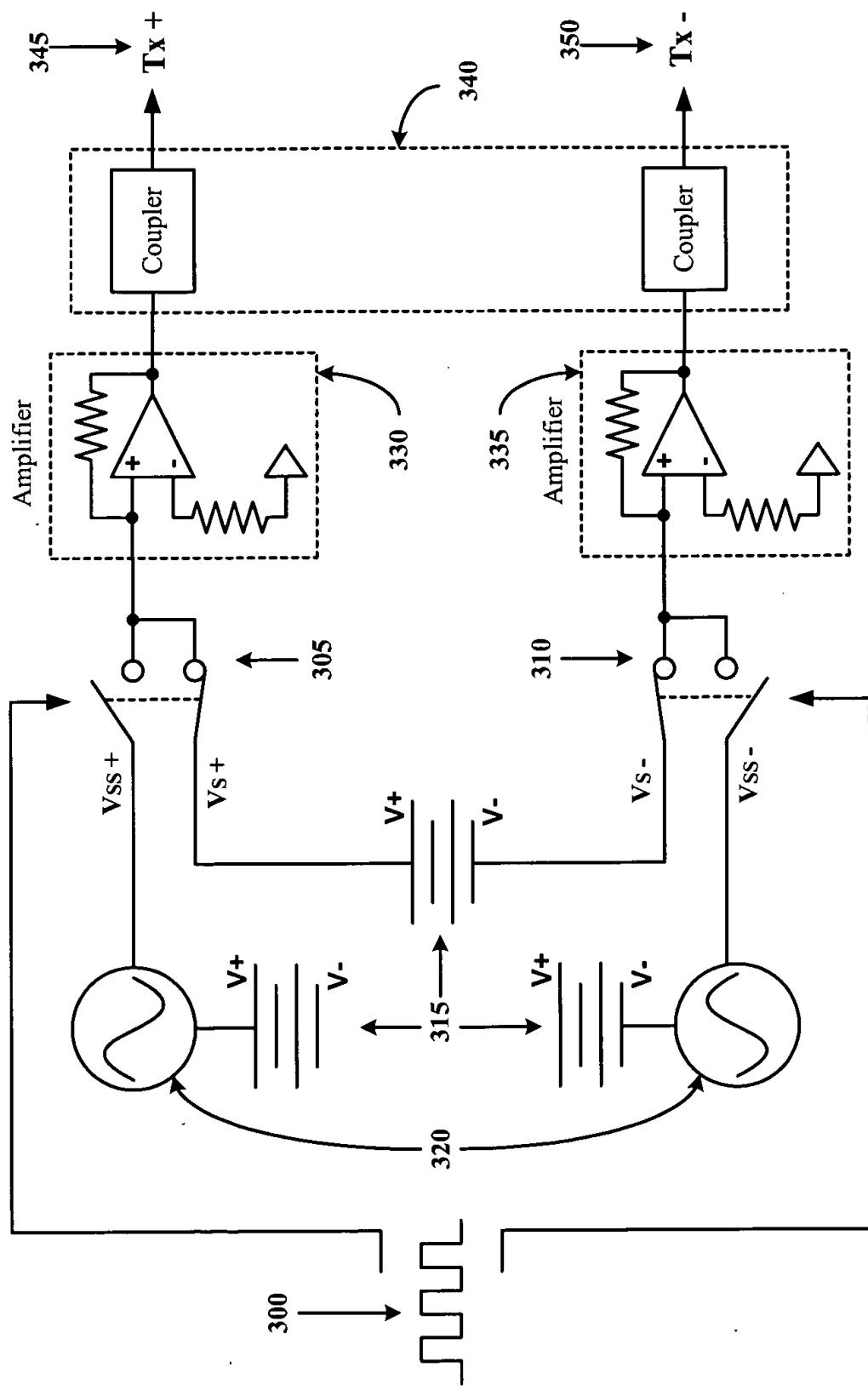
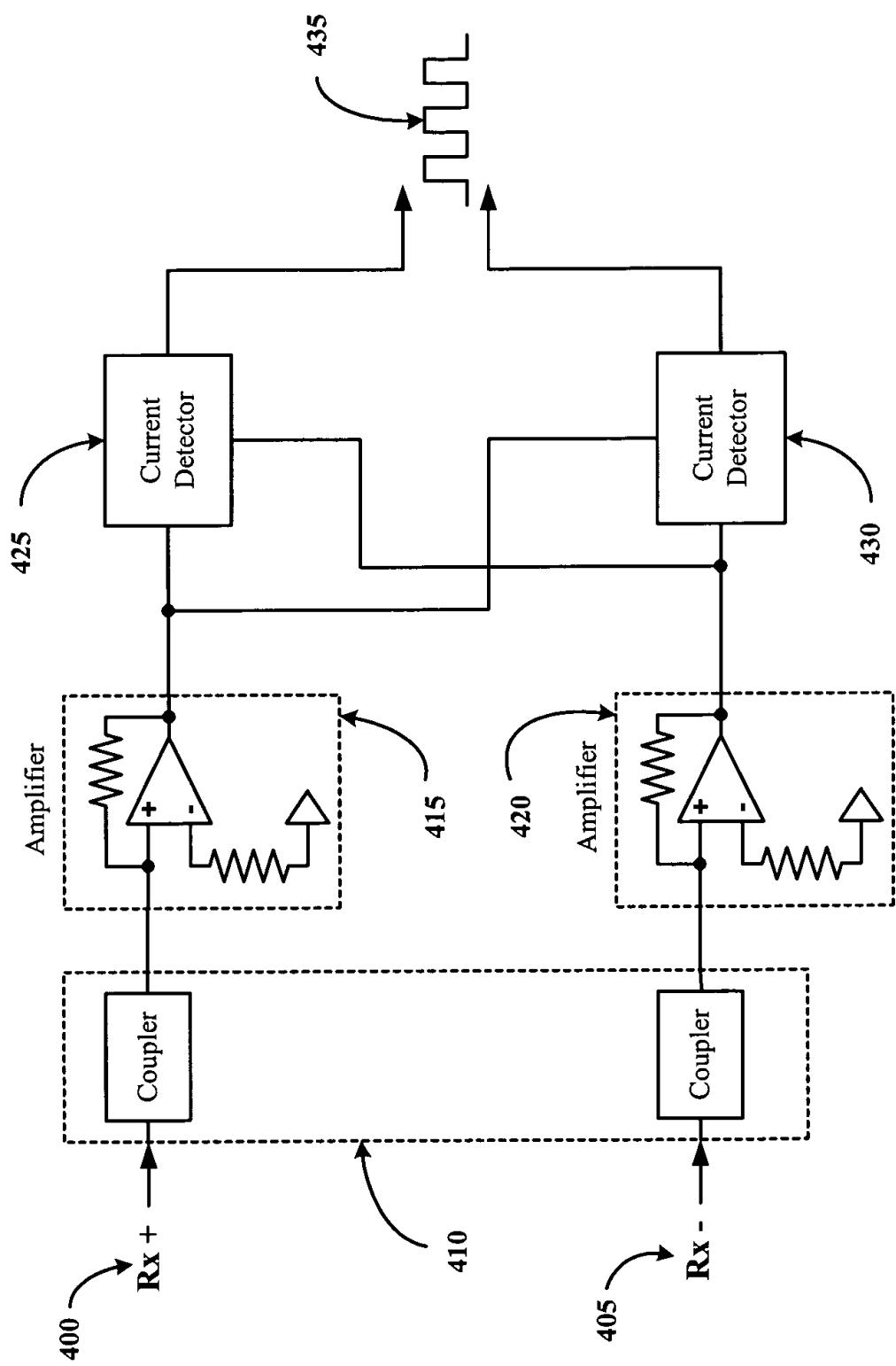


Figure 3 Transmitter



Receiver

Figure 4

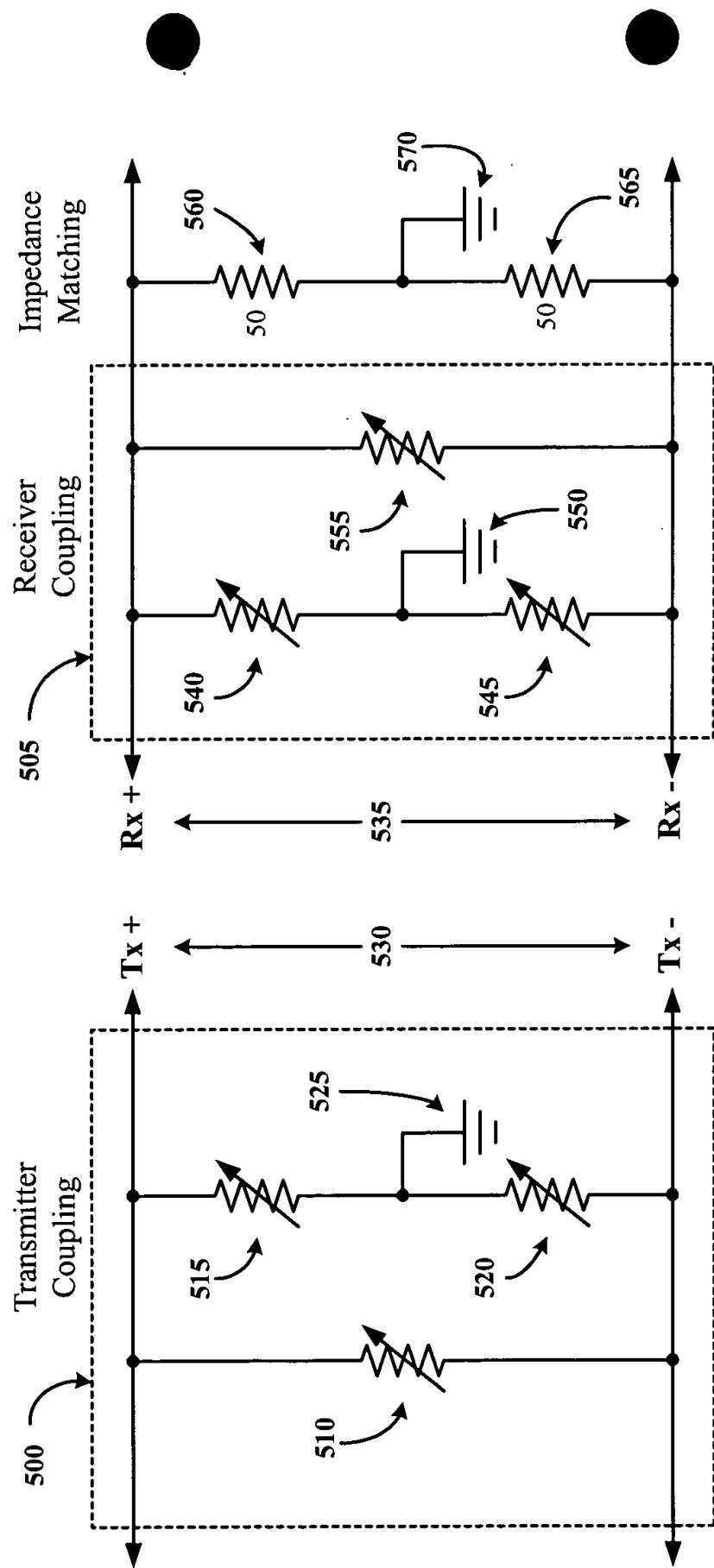
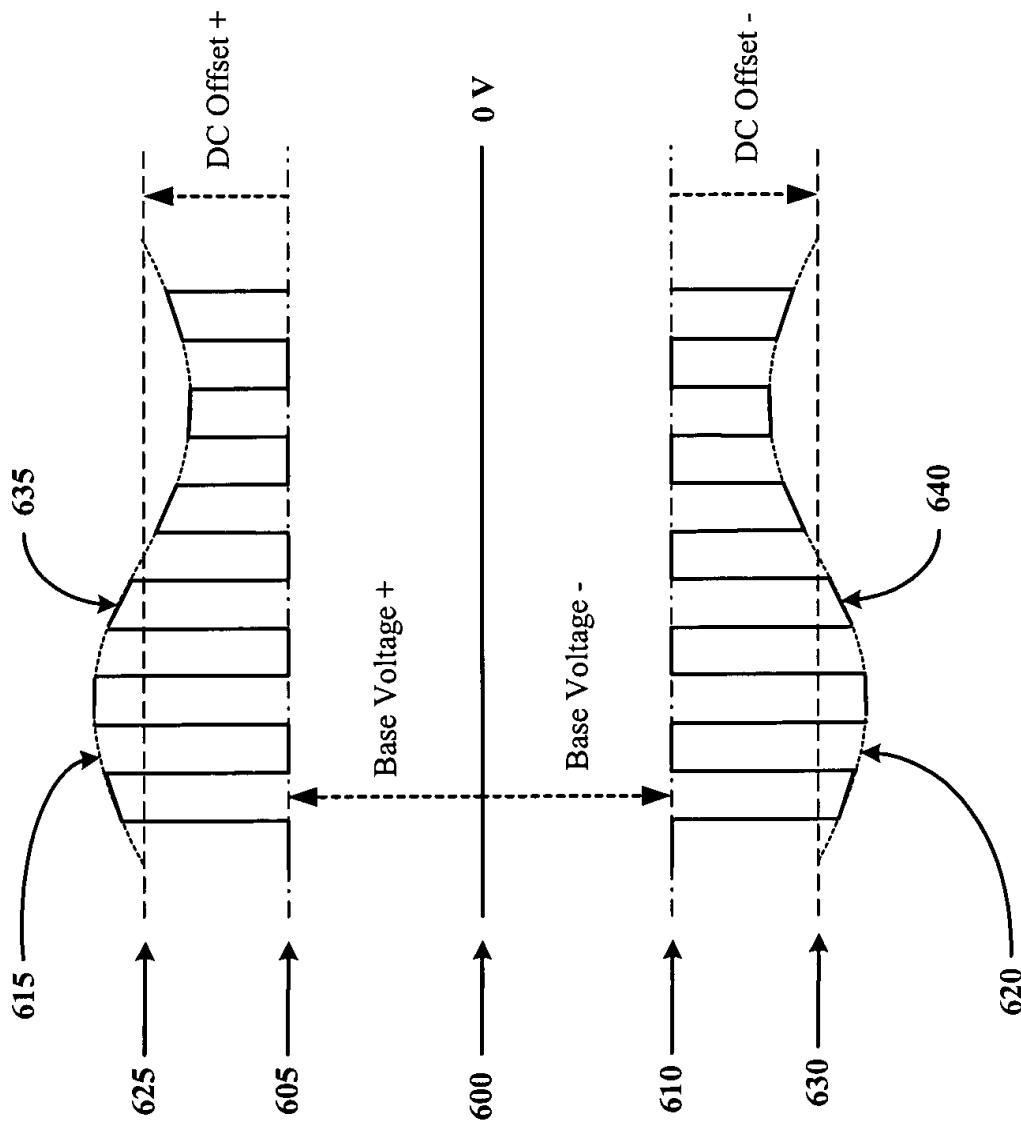


Figure 5  
Twisted Pair  
Coupling



Transmitted Signal  
With Offset Voltage

Figure 6

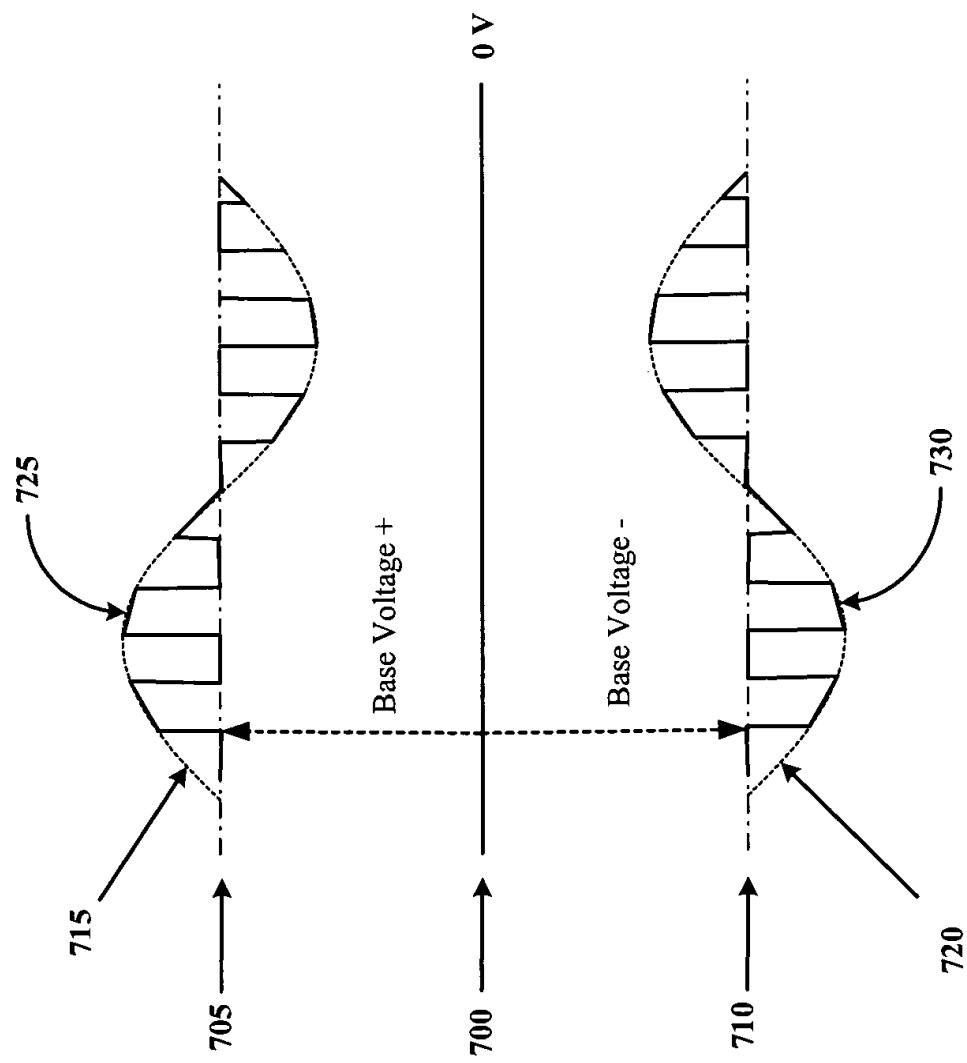


Figure 7

Transmitted Signal  
Without Offset Voltage